GIAN CARLO D. TRILLES

trillesgian29@gmail.com +63 966 421 2170 • Bicol Region, PH
github.com/trillesgian29-cpu • linkedin.com/in/giancarlo-trilles • Portfolio

OJT/INTERNSHIP OBJECTIVE

Computer Engineering student with hands-on experience in Python, C++, embedded systems (Arduino, ESP32), and network configuration. Completed 12 industry certifications and built real projects from concept to deployment. Seeking OJT to apply technical skills in software development, embedded systems, or networking roles.

EDUCATION

Bachelor of Science in Computer Engineering (BSCpE) • Expected 2028

Bicol University Polangui Campus, Albay •Currently: 3rd Year

TECHNICAL SKILLS

Programming: Python, C, C++ | JavaScript, HTML/CSS | Git/GitHub
Embedded Systems: Arduino, ESP32, PWM, Microcontrollers, Sensors | Hardware Testing
Networking: TCP/IP, Subnetting, VLAN, Routing Protocols (RIP, EIGRP, OSPF) | Cisco Packet Tracer
Tools: MATLAB, AutoCAD, Multisim, EasyEDA | Technical Documentation

KEY PROJECTS

Defective Machine Simulation | *Python, DSA, Arrays, Sorting* • 2026

- Developed Python program detecting faulty machines in factory datasets of 1,000+ IDs using arrays, sorting algorithms (bubble, merge, quick), and linked lists
- Reduced anomaly detection time by 40% by implementing binary search after sorting, improving from $O(n^2)$ to O(n log n) complexity
- Applied DSA concepts to real-world quality control scenario, demonstrating practical problem-solving skills

Online Portfolio Website | *HTML, CSS, JavaScript, GitHub Pages* • 2026

- Built and deployed responsive portfolio website from scratch, achieving 100% mobile compatibility across 5+ device sizes
- Implemented scroll-reveal animations using vanilla JavaScript, reducing page load time to under 2 seconds
- Deployed on GitHub Pages with Git version control, maintaining clean commit history with 50+ commits over 2 months

Drag Race Traffic Light System | *Arduino, C++, GPIO, PWM* • 2026

- Programmed Arduino-based Race Light countdown system with 8 LEDs, achieving precise 500ms timing accuracy for professional drag racing simulation
- Implemented interrupt-driven button controls and PWM audio feedback, demonstrating embedded systems proficiency
- Designed and tested circuit with GPIO pin manipulation, completing project in 1 week from concept to working prototype

LABORATORY EXPERIENCE

Completed 15+ hands-on laboratory activities in Circuit Analysis, Digital Electronics, Microcontrollers, and Computer Networks. Configured static and dynamic routing (RIP, EIGRP, OSPF) in Cisco Packet Tracer across 10+ network topologies. Designed and verified logic circuits using Karnaugh maps and Tabular methods, achieving 100% simulation accuracy in simulations in Tinkercad and on a digital trainer; optimized using Boolean simplification and NAND-only conversion.

CERTIFICATIONS & PROFESSIONAL DEVELOPMENT (12 Total)

Python Essentials 1 (DICT Sorsogon) • C++ Essentials 1 (C++ Institute) • Introduction to Embedded Design (Alison Online Course)• Getting Started with Cisco Packet Tracer (NetAcad) • Network Addressing & Troubleshooting (DICT-ITU) • Mobile Technology Fundamentals (DICT Region 2) • AI for Daily Use (Albay AI Institute) • Work Smarter with AI (DICT CAR) • ICpEP National CpE Student Congress (ICpEP BYTE2025 Congress) • Data Analytics Essentials (NetAcad) • Introduction to Data Science (NetAcad)• Electronic Communication & Technology

Available for OJT starting May-June • Flexible schedule • Can commit 240 hours • References available upon request

Full project portfolio, lab activities & certifications available at: **MyPortfolio**